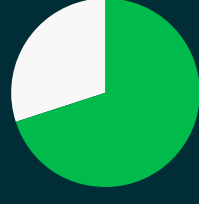




Are you headed for an IC design catastrophe?

Time is Money (and you're likely losing both)

Shrinking market windows and budget constraints make launching new chips on time critical, but complex chip designs and development processes make it nearly impossible.



70%
of all IC chips require a re-spin



Finding bugs post-production =
1,000,000x
more costly



Each month a chip tape out slips =

10% Less Product Revenue

The Problem With Your Process? It's Your Data Storage!



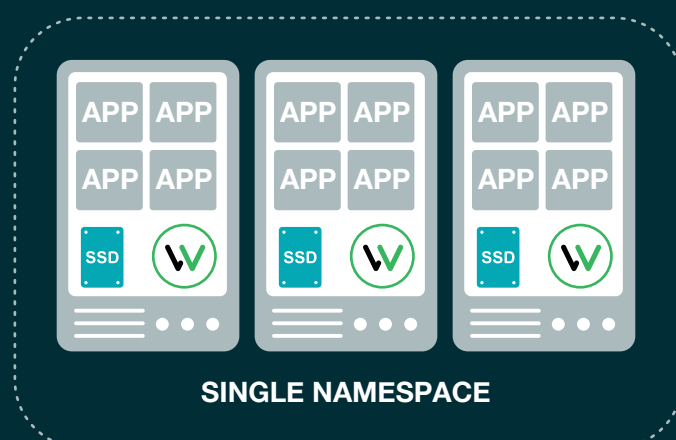
60%
of your design cycle
is verification



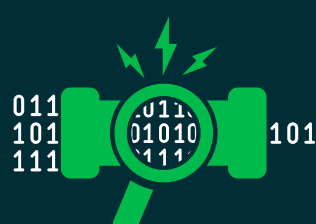
More simulations help avoid re-spins. But storage bottlenecks drastically slow your simulations.

Your design applications are starved of data.

Eliminate the Storage Bottleneck



WekaIO uses the compute resources you already have to create a high-performance scale out file system to remove storage bottlenecks. No need to add costly storage hardware. And your infrastructure is future-proofed.



Any Mixed Workloads

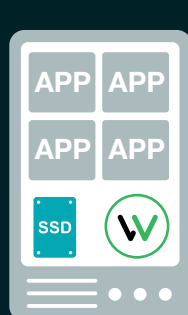


Limitless Scale



Ultra Low Latencies

Drastically Reduce Design Time and Improve Your Bottom Line by Adding WekaIO



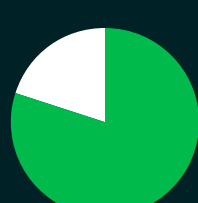
Adding WekaIO

=



50%
reduction on
verification time

+



80%
savings on
storage costs

Shorten Time to Market

Original Timeline

Timeline after WekaIO

25% Savings

WekaIO: A Radically Simple Solution

Revolutionize your IC development to get to market faster and improve returns.



10x
higher IOPS



added footprint

Contact info@weka.io for a Free Trial
or call 408.335.0085